In the Claims:

Please amend claims 2 and 11. The status of the claims is as follows:

1.   (Canceled)

2.   (Currently Amended) ~~The~~ A driving circuit for a liquid crystal display, comprising:

driver output lines connected to an output of a data line driver;

m pieces of block selection signal lines for sequentially selecting m pieces of blocks;

data lines for supplying data to a display area; and

a switch sequentially connecting an ith of said driver output lines to ith, i+2jth, ..., and i+2j×(m-1)th of said data lines in response to signals on said m pieces of block selection signal lines when j is a positive integer smaller than m;

wherein positive and negative voltages that are opposite to each other with respect to a reference voltage are applied to odd-numbered data lines and even-numbered data lines, and

wherein positive and negative polarities of each of said data lines are alternately reversed.

3.   (Original) The driving circuit for a liquid crystal display device according to claim 2,

wherein the j is 1, and

wherein, when one piece of said block selection signal lines is selected, said switch conducts output to two pieces of said data lines adjacent to each other corresponding to the one piece of said block selection signal lines.

4. (Original) The driving circuit for a liquid crystal display device according to claim 2,

wherein data of three colors of red, green, and blue is sequentially inputted to said driver output lines in parallel in order, and

wherein data of three colors of red, green, and blue is sequentially outputted to said data lines in parallel in order.

5. (Original) The driving circuit for a liquid crystal display device according to claim 2,

wherein said driver output lines are connected to the outputs of the data line driver.

6. (Original) The driving circuit for a liquid crystal display device according to claim 4,

wherein said driver output lines are connected to the outputs of the data line driver.

7. (Original) The driving circuit for a liquid crystal display device according to claim 3,

wherein, when one piece of said block selection signal lines is selected, said switch connects two pieces of said driver output lines adjacent to each other corresponding to the one piece of said block selection signal lines to two pieces of said data lines adjacent to each other respectively.

8. (Original) The driving circuit for a liquid crystal display device according to claim 3,

wherein data of three colors of red, green, and blue is sequentially inputted to said driver output lines in parallel in order, and

wherein data of three colors of red, green, and blue is sequentially outputted to said data lines in parallel in order.

9.   (Original)   The driving circuit for a liquid crystal display device according to claim 8,

wherein said driver output lines are connected to the outputs of the data line driver.

10.   (Original)   The driving circuit for a liquid crystal display device according to claim 9,

wherein, when one piece of said block selection signal lines is selected, said switch connects two pieces of said driver output lines adjacent to each other corresponding to the one piece of said block selection signal lines to two of said data lines adjacent to each other respectively.

11.   (Currently Amended)   A The liquid crystal display device having the driving circuit claimed in claim 2 and a display part.